Deep Learning for Communication over Dispersive Nonlinear Channels: Performance and Comparison with Classical Digital Signal Processing

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Abstract—In this paper, we apply deep learning for communication over dispersive channels with power detection, as encountered in low-cost optical intensity modulation/direct detection (IM/DD) links. We consider an autoencoder based on the recently proposed sliding window bidirectional recurrent neural network (SBRNN) design to realize the transceiver for optical IM/DD communication. We show that its performance can be improved by introducing a weighted sequence estimation scheme at the receiver. Moreover, we perform bit-to-symbol mapping optimization to reduce the bit-error rate (BER) of the system. Furthermore, we carry out a detailed comparison with classical schemes based on pulse-amplitude modulation and maximum likelihood sequence detection (MLSD). Our investigation shows that for a reference 42 Gb/s transmission, the SBRNN autoencoder achieves a BER performance comparable to MLSD, when both systems account for the same amount of memory. In contrast to MLSD, the SBRNN performance is achieved without incurring a computational complexity exponentially growing with the processed memory.

I. INTRODUCTION

Deep learning techniques [1] applied to the design of communication systems have been subject to extensive research efforts in recent years. Often, a specific transmitter or receiver function, such as coding, modulation or equalization, is optimized using artificial neural networks (ANN) and deep learning [2]–[6]. For example, a low-complexity fiber nonlinearity compensation block was designed using ANNs in [4]. A deep learning-based receiver in combination with an efficient sequence estimation scheme was proposed for molecular communications [5]. ANNs have also been considered for the equalization module in short reach optical access networks [6]. However, the approach of designing and optimizing the communication system independently on a module-by-module basis can be sub-optimal in terms of the end-to-end performance. Owing to the universal function approximator properties of the ANNs [7], it was proposed in [8], [9] to optimize the complete communication system in a single process spanning from the transmitter input to the receiver output. Such autoencoder systems, implemented as a single deep neural network, have the potential to achieve the optimal end-to-end performance. Autoencoders recently gained popularity in communication scenarios, where the optimum pair of transmitter and receiver or optimum processing modules are not known or prohibitive due to complexity reasons.

In low-cost optical fiber links based on intensity modulation and direct detection (IM/DD), the communication channel is nonlinear with memory, due to the joint effects of chromatic dispersion and square-law photodiode optoelectrical conversion. Such systems are particularly suitable for deep learning-based signal processing due to the absence of optimal algorithms and the presence of stringent computational cost constraints. Simple end-to-end learning systems based on feed-forward neural networks (FFNN) have been investigated for communication over such type of channels and it was verified in experiment that they can outperform conventional pulse amplitude modulation schemes with specific, ubiquitously deployed linear equalizers [10], [11]. More recently, extending the design of [5], an advanced autoencoder for dispersive nonlinear channels which takes into consideration the inter-symbol interference (ISI) has been proposed [12]. The novel communication transceiver based on sliding window processing and end-to-end bidirectional recurrent neural networks (SBRNN) achieved a significant performance improvement compared to previous FFNN autoencoders by exploiting the channel memory. It was shown in [12] that the system can outperform state-of-the-art nonlinear network-based equalizers using multi-symbol receiver processing [6], [13], while requiring significantly fewer trainable parameters.

In this work, we make key modifications in the SBRNN autoencoder, allowing us to increase the system reach or enhance the data rate for shorter distances. First, we generalize the sliding window estimator by introducing new weighting factors at the output of the receiver neural network. This leads to an improved bit-error rate (BER) performance compared to the previous system, where equal weights were assigned [12]. Furthermore, instead of the previously employed ad hoc
approach to bit mapping, we perform bit-to-symbol mapping optimization, thus obtaining an additional BER reduction. Additionally, we compare the end-to-end SBRNN to a benchmark system based on pulse-amplitude modulation (PAM) and classical receiver digital signal processing scheme, tailored for communication over dispersive channels, such as maximum likelihood sequence detection (MLSD) [14]. Our results show that the autoencoder, when processing the same sequence time window, achieves similar BER performance to the MLSD system. However, the SBRNN scheme has a computational complexity which scales only linearly with the channel memory, contrary to the exponential growth of the MLSD scheme.

II. SLIDING WINDOW BIDIRECTIONAL RECURRENT NEURAL NETWORK AUTOENCODER

Following the idea of end-to-end learnable communication systems introduced in [8], the complete fiber-optic link can be implemented as an end-to-end deep neural network [10]. In particular, we employ a sequence processing scheme using a bidirectional recurrent neural network (BRNN) [15], because of the ISI from both preceding and succeeding symbols in our channel model [12]. Figure 1 shows the full chain of BRNN transmitter and receiver including the communication channel.

A. Bidirectional Recurrent Neural Network-Based Transceiver

The function of the neural network-based transmitter is to encode a stream of input messages \((\ldots, m_{t-1}, m_t, m_{t+1}, \ldots)\), \(m_t \in \{1, \ldots, M\}\), each of which is drawn independently from an alphabet of \(M\) total messages, into a sequence of transmit blocks of \(n\) samples \((\ldots, n_{t-1}, n_t, n_{t+1}, \ldots)\). First, we represent the messages \(m_t\) as one-hot vectors \(1_{m_t} \in \mathbb{R}^M\) (whose elements are a “1” at position \(m\) and zeros elsewhere) and feed them for bidirectional processing into the recurrent structure as the input \((\ldots, x_{t-1}, x_t, x_{t+1}, \ldots)\). At the receiver we apply similar bidirectional processing to the distorted samples after propagation. The BRNN technique, identical at both transmitter and receiver, is shown schematically in Fig. 2. In the forward direction, an input \(x_t\) at time \(t\) is processed by the recurrent cell together with the previous output \(h_{t-1}\) to produce an updated output \(h_t\). The procedure is performed on the full data sequence. To adequately handle distortions arising from the succeeding symbols, the structure is also repeated in the backward direction. In this work we concentrate on a low-complexity recurrent cell based on the concatenation between the current input and the previous output. It was shown that in the framework of optical IM/DD communication, the processing capabilities of such cells are similar compared to long short-term memory structures,
without the associated extra complexity [12]. The current cell output is given by

\[ h_t = \alpha_{Tx/Rx} \left( W \left( x_t^T h_{t-1}^T \right)^T + b \right), \]

where \( T \) denotes the matrix transpose, \( W \in \mathbb{R}^{n \times (M+n)} \) and \( b \in \mathbb{R}^n \) (transmitter) or \( W \in \mathbb{R}^{2M \times (n+2M)} \) and \( b \in \mathbb{R}^{2M} \) (receiver) are the weight matrix and bias vector, respectively, and \( \alpha_{Tx/Rx} \) is the utilized activation function in the transmitter/receiver BRNN.

At the transmitter, the input is the one-hot vector representation of the message and the outputs of the RNN cells at the same time instance in both directions are averaged in the \( Tx \) merge stage. Thus, at a time instant \( t \), the transmitter output becomes \( h_t = \frac{1}{2} \left( \overline{h}_t + \overline{h}_t \right) \), with \( \overline{h}_t \) and \( \overline{h}_t \) denoting the outputs of the RNN cells in the forward and the backward directions, respectively. For practical purposes, we employ a transmitter activation function which limits the BRNN outputs to the \([0; \pi/4] \) range. For this reason, we use the clipping activation [10] expressed as

\[ \alpha_{Tx}(x) = \alpha_{ReLU}(x) - \alpha_{ReLU}(x - \pi/4), \]

where \( y_i = \max(0, x_i) \). Note that the forward/backward processing introduces extra latency in the order of a full data sequence. By termination of the sequences, we can limit this processing introduces extra latency in the order of a full data sequence. By termination of the sequences, we can limit this latency to a practically manageable amount (which can be in the range of thousands of symbols).

In contrast, at the receiver, we concatenate the outputs of the two RNN cells in the \( Rx \) merge block. At time \( t \), the output of the BRNN is then expressed by

\[ h_t = \left( \overline{h}_t^T \overline{h}_t^T \right)^T. \]

We employ pure ReLU activation, i.e., \( \alpha_{Rx} = \alpha_{ReLU}(x) \). Additionally, we apply a final softmax layer after the ReLU nodes, resulting in probability vectors at the output \( p_t = \text{softmax}(W_{\text{softmax}} h_t + b_{\text{softmax}}) \) with \( p_t \in \mathbb{R}^M \), \( W_{\text{softmax}} \in \mathbb{R}^{M \times 2M} \) and \( b_{\text{softmax}} \in \mathbb{R}^M \). The softmax function is applied element-wise as

\[ y_i = \exp(x_i) / \sum \exp(x_j). \]

B. Communication Channel

Short reach optical communications are found in many data center, metro and access network scenarios and crucially rely on the simplicity and cost-effectiveness of IM/DD [16]. IM/DD links are also considered as prime candidates for fiber to the x (FTTX) systems, realized for instance via passive optical networks (PONs). Such optical communication links are mainly characterized by the presence of fiber chromatic dispersion [17], which introduces inter-symbol interference (ISI), and nonlinear (square-law) photodiode (PD) detection. Meeting the increasing data rate demands for such systems becomes quite a challenging task because of the limitations imposed by these impairments. The joint effects of ISI and square-law detection render the communication channel nonlinear with memory and one for which the optimal signal processing algorithms at transmitter and receiver are currently absent, to the best of our knowledge.

In the framework of deep learning-based autoencoder design, the communication channel is considered part of the neural network system to facilitate end-to-end training. In our work we use a detailed IM/DD channel model, i.e., an optically un-amplified link, which includes low-pass filtering (LPF) to account for practical hardware limitations at transmitter and receiver, quantization noise from the digital-to-analog (DAC) and analog-to-digital (ADC) converters, Mach-Zehnder modulator (MZM), a photodiode to perform the square-law opto-electrical conversion, electrical amplification noise and optical transmission, modeled by the attenuating and dispersive properties of the fiber. The signal that enters the receiver section of the autoencoder after channel propagation can be expressed as (neglecting the low-pass filtering at the receiver for ease of exposition)

\[ r(t) = \hat{h} \{ \hat{g} \{ x(t) + n_{DAC}(t) \} \} + n_{Rec}(t) + n_{ADC}(t), \]

where \( x(t) \) is the low-pass filtered transmit signal, \( n_{DAC/ADC}(t) \) is additive, uniformly distributed quantization noise from the DAC/ADC, \( \hat{g} \{ \cdot \} \) is an operator describing the effect of the electrical field transfer function of the modulator, \( \hat{h} \{ \cdot \} \) describes the effects of chromatic dispersion, \( n_{Rec}(t) \) is additive Gaussian noise arising from the electrical amplification circuit at the receiver. For more details on the mathematical modeling of the channel components we refer the interested reader to [10] Sec. III-B and [12] Sec. 2.1.

III. AUTOENCODER TRAINING & SEQUENCE ESTIMATION

In this section, we present a summary of the training procedure and explain the sequence estimation scheme in which the trained transceiver is employed. We also give a detailed description of the weighting optimization that we performed in the sliding window algorithm as well as the bit-to-symbol mapping optimization.
A. Training

We perform the system training in a supervised manner by using a set of labeled data. The set of transmitter and receiver BRNN parameters (denoted here by \( \theta \)) is iteratively updated via stochastic gradient descent (SGD) aimed at minimizing the average loss \( \mathcal{L}(\theta) \) over a mini-batch \( S \) from the training set, given by

\[
\mathcal{L}(\theta) = \frac{1}{|S|} \sum_{1_{m,t} \in S} \ell(1_{m,t}, f_{\text{BRNN},m,t}(\ldots, 1_{m,t}, \ldots)),
\]

where \( f_{\text{BRNN},m,t}(\ldots, 1_{m,t}, \ldots) \) is the output of the end-to-end BRNN corresponding the one-hot vector \( 1_{m,t} \) input to the transmitter at the same position and \( \ell(x, y) \) denotes the loss function. In this work, we use the cross entropy as a loss function, which is defined as \( \ell(x, y) = - \sum x_i \log(y_i) \).

The SGD is implemented using the Adam algorithm [13]. In the following we provide a detailed step-by-step description of the training procedure.

First, a set of \( Z = 250 \) different sequences of \( T_{\text{train}} = 10^5 \) random input messages \( m_{i,j} \) is generated, with \( i \in \{1, \ldots, Z\} \) and \( j \in \{1, \ldots, T_{\text{train}}\} \). At the beginning of the training, the outputs \( \tilde{h}_{t-1} \) and \( \tilde{h}_{t+1} \) in the forward and backward directions of the BRNN are initialized to \( 0 \). At an optimization step \( s \), the mini-batch \( S \) of messages \( m_{i,(s-1)\nu+1}, \ldots, m_{i,\nu\nu} \), for \( 1 \leq i \leq Z \) and fixed \( \nu = 10 \), is processed by the transmitter BRNN to obtain the blocks \( \tilde{h}_{i,(s-1)\nu+1}, \ldots, \tilde{h}_{i,\nu\nu} \). Before feeding them into the communication channel, these blocks are transformed into a sequence \( \tilde{h}_{1,(s-1)\nu+1}, \ldots, \tilde{h}_{1,\nu\nu}, \tilde{h}_{2,(s-1)\nu+1}, \ldots, \tilde{h}_{2,\nu\nu}, \ldots, \tilde{h}_{B,(s-1)\nu+1}, \ldots, \tilde{h}_{B,\nu\nu} \).

At the input of the receiver, this transformation is reversed and the received blocks \( y_{i,(s-1)\nu+1}, \ldots, y_{s,\nu\nu} \) are applied to the BRNN, obtaining output probability vectors \( p_{i,(s-1)\nu+1}, \ldots, p_{i,\nu\nu} \). Then, in accordance with (1), the cross entropy loss between inputs and outputs is averaged over the whole mini-batch and a single iteration of the optimization algorithm is performed. Every 100 steps of the optimization, we re-initialized the outputs \( \tilde{h}_{t-1} \) and \( \tilde{h}_{t+1} \) in the forward and backward passes of the BRNN to \( 0 \) in an attempt to avoid local minima. Note that using validation data we confirmed that the convergence of the loss during training was achieved within 100,000 iterations, the maximum number of iterations that we allowed.

After training of the autoencoder, we employ it in the sliding window sequence estimation algorithm proposed in [5]. It is important to mention that for the training set a Mersenne twister was used as a random number generator. To ensure that during training we do not learn parts or construction rules of the pseudo-random sequence [19] and that training and testing datasets originate from different sources we used a Tausworthe [20] random number generator to generate an independent testing set of data using different 250 sequences of 10,000 randomly chosen messages.

B. Sliding Window Sequence Estimation Algorithm

Figure 3 shows a basic schematic of the sliding window sequence estimation algorithm, where the autoencoder is represented by the blocks Tx BRNN, channel and Rx BRNN. For a given sequence of \( T + W - 1 \) test messages, the transmitter BRNN encodes the full stream of input one-hot vectors \( 1_{m,1}, \ldots, 1_{m,T+W-1} \). The obtained waveform is then subject to the channel, yielding the sequence of received blocks \( y_1, \ldots, y_{T+W-1} \). At a time \( t \), the receiver BRNN processes the window of \( W \) blocks \( y_t, \ldots, y_{t+W-1} \), transforming them into \( W \) probability vectors \( p_1^{(t)}, \ldots, p_{t+W-1}^{(t)} \) via its final softmax layer. Then it slides one time slot ahead to process the blocks \( y_{t+1}, \ldots, y_{t+W} \). The final output probability vectors for the first \( W - 1 \) blocks are given by

\[
p_i = \frac{1}{W} \sum_{k=0}^{i-1} p_{i-k}^{(t)}, \quad i = 1, \ldots, W - 1.
\]

The final probability vectors for the remaining \( y_W, \ldots, y_T \) blocks in the received sequence are obtained as

\[
p_i = \sum_{k=0}^{W-1} a^{(q)} p_{i-k}^{(t)}, \quad i = W, \ldots, T,
\]

where \( a^{(q)} \geq 0 \), \( q = 0, \ldots, W - 1 \) and \( \sum_{q=0}^{W-1} a^{(q)} = 1 \) are the weighting coefficients for the softmax probability output of the receiver BRNN. Equal weights \( a^{(q)} = \frac{1}{W} \) were previously assumed in both [5] and [12]. Note that the final \( W - 1 \) blocks \( y_{T+1}, \ldots, y_{T+W-1} \) from the received sequence are not fully estimated and we do not include them in the subsequent block error counting. We choose \( T \gg W \) and thus there is only a negligible reduction in the data rate of the scheme.
We now present a concrete example of the first few steps in the operation of the sliding window processor with \( W = 3 \) based on Fig. 3. At \( t = 1 \), the receiver BRNN processes the blocks \((y_1, y_2, y_3)\) and outputs probability vectors \( \left( p_1^{(1)}, p_2^{(1)}, p_3^{(1)} \right) \). The receiver has generated all estimates for the received block \( y_1 \) and we have \( p_1 = p_1^{(1)} \). Next, the receiver BRNN shifts a single slot to process \((y_2, y_3, y_4)\) at \( t = 2 \) and generates \( \left( p_2^{(2)}, p_3^{(2)}, p_4^{(2)} \right) \). We have gathered all information about \( y_2^{(2)} \) and compute \( p_2 = \frac{1}{2} (p_2^{(1)} + p_2^{(2)}) \) as a final estimate. Similarly, after \( t = 3 \), a final probability vector \( p_3 = a^{(0)} p_3^{(3)} + a^{(1)} p_3^{(2)} + a^{(2)} p_3^{(1)} \) is computed for \( y_3 \).

The sliding window processing carries on for the remainder of the blocks.

In this work, we optimize the weight coefficients \( a^{(q)} \) in order to improve the overall error rate performance. We perform the optimization offline by picking a representative test sequence and \( I \) corresponding BRNN output probability vectors \( \{p_1, p_2, \ldots, p_T\} \). We then perform the optimization based on Fig. 3: At \( t \) for which we collect all corresponding BRNN output probability vectors \( p_i^{(t-k)} \), with \( i = W, \ldots, T \) and \( k = 0, \ldots, W - 1 \). We find the best set of coefficients \( a = (a^{(0)}, \ldots, a^{(W-1)}) \) by minimizing the average cross entropy

\[
\widehat{c} \left( a \right) := - \frac{1}{T - W + 1} \sum_{i=W}^{T} \sum_{q=0}^{W-1} \log \left( \sum_{i=0}^{W-1} a^{(q)} p_i^{(i-q)} \right)
\]

between the input one-hot vectors \( 1_m,1_W, \ldots, 1_m,T \) and the estimated final output probability vectors \( p_W, \ldots, p_T \), using the constrained optimization problem

\[
a_{\text{opt}} = \arg \min_a \widehat{c}(a) \quad \text{s.t.} \quad a^{(q)} \geq 0, \quad \sum_{q=0}^{W-1} a^{(q)} = 1
\]

where \( q = 0, \ldots, W - 1 \) and \( \widehat{c} \) is the average cross entropy between the input one-hot vectors and the estimated final output probability vectors.\(^1\) We apply the optimized coefficients in the estimation for all sequences in the testing set.

After the algorithm estimates the final probability vector for a given received block, we can perform decision on the transmitted message. We count a block error when \( \arg \max(1_m,i) \neq \arg \max(p_i) \). The block error rate (BLER) for the transmitted sequence is given by

\[
\text{BLER} = \frac{1}{|T_e|} \sum_{t \in T_e} \mathbb{1}_{\{\arg \max(1_m,i) \neq \arg \max(p_i)\}},
\]

where \(|T_e|\) is the number of fully estimated messages in the test sequence and \( \mathbb{1}_{\{\cdot\}} \) denotes the indicator function, equal to 1 when the argument is satisfied and 0 otherwise.

Figure 4 (top) shows the improvement in BLER at different distances for \( W = 10 \) after optimization of the coefficients \( a^{(q)} \) in (2). As a reference we used the BLER performances when equal weights \( a^{(q)} = \frac{1}{W}, q = 0, \ldots, W - 1 \) are assigned. Note that the set of coefficients is optimized separately for each distance. We can see that the benefit from weight optimization becomes more pronounced as the distance increases. The \( a^{(q)} \) assignments after optimization at 100 km are depicted in Fig. 3 (bottom). Larger weights are assigned to the middle indices, indicating that a received block is estimated with a higher accuracy by the receiver when relatively equal amount of pre- and post-cursor interference is captured.

### C. Bit Labeling Optimization

The input and output of the ANN are non-binary messages \( m \in \{1, \ldots, M\} \) and \( \hat{m} \). In order to guarantee the low bit error rates (BERs) in the range \( 10^{-12} \) to \( 10^{-15} \), we require forward error correction (FEC). For complexity reasons, FEC schemes in optical communications are usually binary \(^2\) and often based on hard-decision decoding (HDD), in particular in IM/DD applications. An overview of HDD decoding schemes and their decoding capabilities is given in \(^{22}\). To convert between the ANN messages and the FEC encoder/decoder output/input, we need a bit labeling function \( \varphi : \{1, \ldots, M\} \to \mathbb{F}_2 \), which maps a message \( m \) to a binary vector \( b = (b_1, \ldots, b_B) \), \( b_i \in \mathbb{F}_2 = \{0,1\} \) of length \( B \geq \lceil \log_2(M) \rceil \). Usually, we select \( M = 2^B \) such that \( M = 2^B \).

Finding a bit labeling that minimizes the bit error rate is an NP-hard task that is usually solved using combinatorial optimization, e.g., a bit switching algorithm \(^{23}\). Here, we use the Tabu search algorithm \(^{24}\) with a Tabu list of size 256. We start with a random bit labeling and, using the outcome of the validation run, namely the estimated probabilities \( P(\hat{m}|m) \), we use the expected error rate as cost function. For a given bit labeling, the Tabu search tries all possible combinations of two elements

\(^1\)The optimization problem is convex and can easily be solved numerically.

\(^2\)Optical links are typically binary, so we limit our discussion to binary FEC schemes.
and computes the resulting expected BER. We select the combination that leads to the lowest expected BER and is not in the Tabu list. The Tabu list is then updated with this new assignment in a first-in/first-out fashion. After a pre-defined number of iterations, the overall best assignment is selected. We compare the resulting bit mapping with the trivial BER lower bound BER > P(\tilde{m} \neq m|m)/B, assuming that each symbol error yields exactly a single bit error.

Alternatively, [25] suggests to modify the autoencoder to encode a set of B bits into a set of B decoded bits. In this case, the loss function minimizes the average bit error rate. This is a viable alternative, however, we have found that in some circumstances, the training can get stuck in a local minimum, especially when the channel input is heavily constrained. In this work, we optimize the bit mapping which gives already performance close to the lower bound.

IV. MAXIMUM LIKELIHOOD SEQUENCE DETECTION

In order to establish a relevant benchmark for the performance of the SBRNN autoencoder, we investigate a reference M-PAM transmission system (M ∈ {2, 4}) with a receiver based on maximum likelihood sequence detection (Tx-PAM&Rx-MLSD). For this comparison, it is worth noting that the autoencoder performs an optimization of both the transmitter and receiver designs, while in contrast, the transmitter is assumed fixed in the Tx-PAM&Rx-MLSD scheme. However, for optical IM/DD transmission receivers based on MLSD have been widely considered [26], [27], and represent a valid performance reference scheme.

The MLSD receiver assumes that the channel behaves according to a Markov process, thus facilitating the use of the Viterbi algorithm [28] which selects

$$\hat{x} = \arg \max_{x} \hat{p}(y|x),$$

where \(x = (x_1, x_2, \ldots, x_{N_x})\) is the sequence of transmitted PAM symbols, \(y = (y_1^1, y_1^2, \ldots, y_1^N_y, y_2^1, y_2^2, \ldots, y_2^N_y, \ldots, y_{N_y}^1, y_{N_y}^2, \ldots, y_{N_y}^N_y)\) is the sequence of corresponding received samples. Here \(N_y\) and \(N_x\) denote the number of PAM symbols in the transmitted sequence and number of samples per symbol, respectively. The joint distribution \(\hat{p}(y|x)\) is an approximation of the true channel likelihood \(p_{Y|X}(y|x)\), whose accuracy depends on the number of states and metric used in the Viterbi processor. For IM/DD systems, a more convenient option for an MLSD receiver is to operate on the distribution \(p(Y|X)\), where \(Y = (\tilde{y}_1^1, \tilde{y}_1^2, \ldots, \tilde{y}_1^N_y, \tilde{y}_2^1, \tilde{y}_2^2, \ldots, \tilde{y}_2^N_y, \ldots, \tilde{y}_{N_y}^1, \tilde{y}_{N_y}^2, \ldots, \tilde{y}_{N_y}^N_y)\) with \(\tilde{y}_l^k = \sqrt{y_l^k}\) for \(l = 1, \ldots, N_x\) and \(k = 1, 2, \ldots, N_y\). Indeed, if we define the channel state as

$$\sigma_k \doteq (x_{k-\frac{\mu}{2}}, x_{k-\frac{\mu}{2}+1}, \ldots, x_{k-1}, x_{k+1}, \ldots, x_{k+\frac{\mu}{2}})$$

(3)

where \(\mu\) is the number of pre- and post-cursor symbols determining the channel memory, the distribution of \(y_l^k\) conditioned on \((\sigma_k, x_k)\) was shown to be well approximated by a Gaussian with equal variances [26]. Under such an approximation, and the assumption of uncorrelated samples \(y_l^k\) conditional to \((\sigma_k, x_k), p(Y|X)\) can be suitably factorized via the channel state definition in (3) for the use in a Viterbi processor.

We can thus define the branch metric of the Viterbi trellis at time \(k\) as

$$\lambda_k(\sigma_k, x_k) = \sum_{l=1}^{N_x} \left( \sqrt{y_l^k} - \nu_k^l(\sigma_k, x_k) \right)^2$$

(4)

where \(y_l^k\) is the \(l\)-th out of \(N_x\) samples within the \(k\)-th symbol period, and \(\nu_k^l(\sigma_k, x_k)\) is given by

$$\nu_k^l(\sigma_k, x_k) = \mathbb{E} \left\{ \sqrt{y_l^k(\sigma_k, x_k)} \right\},$$

(5)

with \(y_l^k(\sigma_k, x_k)\) indicating the value of the received sample \(y_l^k\) when the pair \((\sigma_k, x_k)\) occurs. The square-root metric in (4) was introduced in [27] and was proven to be a convenient low-complexity alternative to the histogram-based metric, yet with comparable performance [26]. At the end of each given window of \(T\) symbols, the Viterbi processor makes a decision on the previous \(T\) transmitted symbols by minimizing the sequence metric \(\Lambda_T = \sum_{k=1}^{T} \lambda_k(\sigma_k, x_k)\) over all surviving state sequences \([\sigma_1, \sigma_2, \ldots, \sigma_T]\).

The schematic diagram of the system model employing MLSD is shown in Fig. 5 (with the channel parameters taken from Table I). At the transmitter, a Gray-labelled PAM2 ((0; π/4)) or PAM4 ((0; π/12; π/6; π/4)) mapper was used, followed by pulse-shaping at 2 samples per symbol by a root-raised cosine (RRC) filter with a roll-off factor of 0.25. DAC/ADC rates of 84 GSa/s and 42 GSa/s are assumed for the PAM2 and PAM4 cases, respectively, resulting in a fixed data rate of 42 Gb/s for both systems. A signal corresponding to a sequence of \(10^5\) symbols was transmitted through the channel described in Sec. II-B. At the receiver, the estimation of the \(\nu_k^l(\sigma_k, x_k)\) was first done computing (5) over a sequence of \(10^5\) symbols. The received signal, sampled at \(N_y = 2\) samples per symbol, is then passed directly to the Viterbi processor which performs the MLSD.
Fig. 6. Bit error rate as a function of transmission distance for the 42 Gb/s SBRNN autoencoder and $M$-PAM & Rx MLSD systems ($M \in \{2, 4\}$). In the case of MLSD $\eta = \mu \log_2(M)$, where $\mu$ represents the number of pre- and post-cursor PAM symbols defining one of $M^\mu$ channel states. In the case of SBRNN $\eta = W \log_2(M)$ is the number of bits inside the processing window. The solid SBRNN curves with square marks (■) include the bit-to-symbol mapping optimization described in Section III-B, while circle marks (○) give the BER with a randomly chosen bit mapping and cross marks (×) show the lower bound on the BER (at most 1 bit error per symbol).

<table>
<thead>
<tr>
<th>TABLE I</th>
<th>CHANNEL SIMULATIONS PARAMETERS</th>
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<tbody>
<tr>
<td>Parameter</td>
<td>Value</td>
</tr>
<tr>
<td>DAC/ADC rate</td>
<td>84 or 42(PAM4) GSa/s</td>
</tr>
<tr>
<td>LPF bandwidth</td>
<td>32 GHz</td>
</tr>
<tr>
<td>DAC/ADC ENOB</td>
<td>6</td>
</tr>
<tr>
<td>Fiber dispersion parameter</td>
<td>17 ps/nm/km</td>
</tr>
<tr>
<td>Fiber attenuation parameter</td>
<td>0.2 dB/km</td>
</tr>
<tr>
<td>Receiver noise power</td>
<td>0.245 or 0.127(PAM4) mW</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>TABLE II</th>
<th>AUTOENCODER SIMULATIONS PARAMETERS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Parameter</td>
<td>Value</td>
</tr>
<tr>
<td>$M$</td>
<td>64</td>
</tr>
<tr>
<td>$n$</td>
<td>48</td>
</tr>
<tr>
<td>Test sequence length</td>
<td>10000</td>
</tr>
<tr>
<td>Processing window $W$</td>
<td>2 or 10</td>
</tr>
<tr>
<td>Symbol rate</td>
<td>7 GSym/s</td>
</tr>
<tr>
<td>Information rate</td>
<td>6 bits/symbol</td>
</tr>
</tbody>
</table>

V. PERFORMANCE AND COMPLEXITY

In this section, we make a comparison between the BER performance of the SBRNN autoencoder and the Tx-PAM4&Rx-MLSD systems at a fixed data rate of 42 Gb/s and also discuss their computational complexity. Table II lists the parameters used for the channel simulation, identical for both schemes.

A. BER Performance

The performance of the SBRNN autoencoder is numerically evaluated using the design parameters given in Table II. Note that weight optimization in the sliding window estimation is done as described in Sec. III-B. An input sequence of messages, each from a set of $M = 64$ (6 bits), is encoded by the transmitter BRNN into a sequence of symbols (blocks) of $n = 48$ samples. We assume an oversampling factor of 4 over the 84 GSa/s sampling rate of the DAC and, thus, simulation is performed at 336 GSa/s. The symbol rate of the system becomes 7 GSym/s and information is transmitted at the rate of 42 Gb/s. In an attempt to set up a fair comparison between the investigated systems, we fixed a parameter $\eta$, which in the case of SBRNN, corresponds the number of information bits processed inside the sliding window $\eta = W \log_2(M)$. For the PAM transmitter with MLSD receiver, this number denotes the amount of bits contained within a channel state in the Viterbi algorithm $\eta = \mu \log_2(M)$, where $M \in \{2, 4\}$ is the PAM order and $\mu$, already defined in Sec. IV, denotes the number of post- and pre-cursor PAM symbols which form a Viterbi state. It is important to stress that in Tx-PAM2&Rx-MLSD and Tx-PAM4&Rx-MLSD, a fixed $\eta = 12$ also means using the same number of Viterbi states (4096). This implies Tx-PAM4&Rx-MLSD accounting for a decreased amount of memory compared to Tx-PAM2&Rx-MLSD.

Figure 6 shows the BER performance of the examined systems as a function of transmission distance. We see that for $\eta = 12$, the SBRNN autoencoder and Tx-PAM2&Rx-MLSD have comparable performance at all examined distances beyond 50 km. Both systems outperform the Tx-PAM4&Rx-MLSD, where the obtained BER is below the 6.7% hard-decision forward error correction (HD-FEC) threshold up to 30 km. This is due to the decreased sensitivity of the PAM4 signal and Viterbi memory ($\mu = 6$). Moreover, the results indicate that assuming a wider processing window of $\eta = 60$ for the SBRNN leads to a significant BER improvement.

Note that we obtained the BER of the SBRNN autoencoder using the bit-to-symbol mapping algorithm described in Sec. III-C for an additional comparison Fig. 6 also shows the BER performance of the system when the
ad hoc approach of assigning the Gray code to the input \( m \in \{1, \ldots, M\} \) is employed as well as the trivial case of a single block error resulting in a single bit error. We see that by performing the bit-to-symbol mapping optimization the performance of the system is improved, leading to an increase in the achievable distances below the HD-FEC threshold to 50 km and 70 km for the cases of \( \eta = 12 \) and \( \eta = 60 \), respectively.

B. Computational Complexity

We study the computational complexity of the two systems using the floating point operations per decoded bit (FLOPS\(_{\text{pdb}}\)) as a common metric. In the case of the SBRNN autoencoder, we count FLOPS in matrix multiplications, bias additions and element-wise nonlinear activations in both direction of the recurrent structures as well as multiplications and additions in the final probability vector estimations. At the transmitter, the number of FLOPS needed to encode a bit is given by

\[
\text{FLOPS}_{\text{pdb}}^{\text{SBRNN-TX}} = \frac{2n(2(M + n) + 1)}{\log_2 M},
\]

where \( M \) and \( n \) are hyper-parameters of the neural network, i.e. pre-defined design choices which do not change with the processing memory. Correspondingly, at the receiver we have

\[
\text{FLOPS}_{\text{pdb}}^{\text{SBRNN-RX}} = \frac{W(24M^2 + 8Mn + 5M + 2)}{\log_2 M},
\]

which exhibits a linear dependence of the floating point operations on the processing window \( W \). It is worth noting that often in practice some portion of the trained neurons are inactive, thus reducing the actual amount of FLOPS. Furthermore, one-hot vector multiplications at the transmitter in principle can be substituted by embedding lookups.

For the Viterbi processor, the amount of floating point operations scales linearly with the number of states in the trellis which is equal to \( M^\mu \). Assuming a fully populated trellis, the Viterbi processor performs for each trellis section \( M^{\mu+1} \) branch metric calculations, \( M^{\mu+1} \) additions, and \( M^{\mu+1} \) comparisons (other than the storage of \( M^\mu \) sequences). As for the computation of the branch metric, a total of \( 3N_s + (N_s - 1) \) FLOPS are required (assuming addition, multiplication, and square-root use 1 FLOP each). Moreover, we will here account for each comparison as 1 FLOP and consider the additional set of comparisons required for the selection of the most likely sequence at the end of the Viterbi decoding negligible. Thus, the overall number of required FLOPS per decoded bit is approximately given by

\[
\text{FLOPS}_{\text{pdb}}^{\text{MLSD}} \approx \frac{9M^{\mu+1}}{\log_2 M} \quad (6)
\]

for \( N_s = 2 \). Importantly, the expression in (6) indicates that the number of FLOPS\(_{\text{pdb}}^{\text{MLSD}}\) exhibits an exponential dependence on the processing memory of the detector. In presence of strong inter-symbol interference this may result in computationally prohibitive demands or increasingly sub-optimal performance.

VI. CONCLUSIONS

We improved the performance of the SBRNN autoencoder by conducting an essential optimization of the bit-to-symbol mapping function using Tabu search combinatorial algorithm. Furthermore, we proposed an offline method for optimizing the weight assignments in the sliding window estimation algorithm which leads to BER reduction at longer distances. In a comparative study of performance and computational complexity with schemes based on PAM modulation and maximum likelihood sequence detection, our results indicate that for a fixed memory in the receiver algorithms, the SBRNN autoencoder achieves BER close to the scheme based on PAM2. It outperforms the PAM4 scheme, which is associated with higher sensitivity to noise in the system. Importantly, in terms of floating point operations per decoded bit, the autoencoder has linear dependence on the assumed memory, unlike the benchmark scheme where the dependence is exponential.

REFERENCES


